

## MODEL 3380D

### KEY FEATURES

- 50/100 MHz clock rate
- 50/100 Mbps data rate
- 256 logic I/O pins (max. 576 pins)
- Up to 256 sites parallel test
- 32/64/128M pattern memory
- Various VI source
- Flexible HW-architecture  
(Interchangeable I/O, VI, ADDA,)
- Real parallel trim/match function
- Time & Frequency Measurement Unit (TFMU)
- AD/DA test (16/24bits option)
- SCAN test option (max. 2G M/chain)
- ALPG test option for embedded memory
- STDF tools support
- Test program/pattern converter (J750, D10, S50, E320, SC312, V7, TRI-6020)
- Direct mount probe card is compatible with 3360P direct mount probe card
- Cable mount DUT card is compatible with 3360D/3360P cable mount DUT card (FT/CP)
- CRAFT C/C++ programming language
- Software is compatible with 3360/3360P
- User friendly Windows 7 environment

## VLSI TEST SYSTEM MODEL 3380D

In order to cope with the IC testing trend of high-speed, numerous pins and complex functions in the future, the newest generation of Chroma's VLSI testers, 3380D/3380P/3380, have adopted a more flexible architecture with higher integration density and powerful functions.

The 3380D/3380P/3380 test systems have 4 wires HD VI source and any-pins-to-any-site high parallel test (multi-sites test) functions (256 I/O pins to test 256 ICs in parallel) that can meet the upcoming higher IC testing demands.

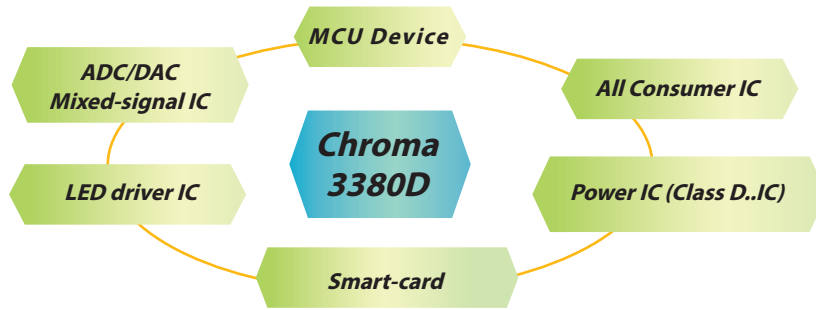
The test systems also have built in DC power rack design to provide a small footprint/clear power ATE to become a very competitive price/performance ratio test system.

The 3380D will be ideally for IoT applications, especially for those cost sensitive devices such as MCU, MEMS, and MCU with integrated functions. The VLSI test systems, including the 3380D/3380P/3380 series, have been widely adopted in China market.



**Chroma**

Full Applications to meet varied ICs such as Logic, ADDA, RF(MCU), LED, Power, ALPG, Match, etc.



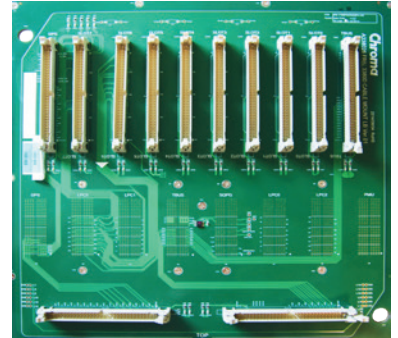
## SPECIFICATIONS

Model	3380D
<b>Standard Specification</b>	
Clock Rate	50/100 Mhz
Data Rate	50/100 Mbps
Pin Channels	256 Pins
Pattern Memory	32M(S) / 64 & 128M (option)
Parallel Testing Capability	256 DUTs
EPA	± 500ps
Resource Per Pin Architecture	Yes
VI source	8CH : MXDPS 16CH : MLDPS-16(S) / MXUVI / MXREF 32CH : MLDPS
PMU (± 48V, ± 100 mA)	16 Channels /board
HV-Pins driver ( +5.9V to +13.5V)	4 channels /board
PPMU (-2V~+ 6V, ± 32 mA)	Per Pin (FIMV/FVMI)
Programmable Active Load ( ± 12 mA)	Per Pin
TFMU (Time/Freq Measure unit:Max 400Mhz)	Per Pin
Free-run Clock ( Max: 200Mhz)	Per Pin
Windows Environment	Windows 7
Programming Language	C/C++
<b>Test Option Specification</b>	
AD/DA Converter Test Option ( MXAWI/MXAWI2 )	4 AWG/ 4 DIG ( 16/24bits)
Mixed- Signal test option ( PXI )	24bits, 200MS/s
MXUVI ( DPS ± 12V, ± 1A, CG ± 4A )	16 Channels /board
MXDPS ( DPS ± 16V, ± 2A )	8 Channels /board
MXREF ( DPS ± 48V, ± 250mA, CG ± 1A )	16 Channels /board
MLDPS (DPS + 12V/± 500mA, ± 6V/± 1A , CG max ± 8 A )	32 Channels /board
SCAN Option	1G bits/ chain
ALPG Memory Test Option	16X, 16V, 16D /board
<b>System And Dimension</b>	
Power consumption Max	2KVA (VI Option to Max. 3KVA)
Test Head	W365 x D586 x H412 mm ( Max:45Kg)
Power Box	W220 x D372 x H187 mm ( Max:15Kg)

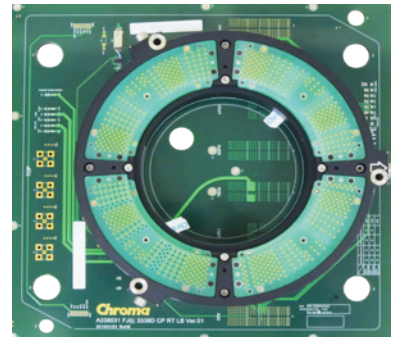
Note 1: "Cable-Mount" as standard, "Direct-Mount" as option.

\* All specifications are subject to change without notice.

## Compatible Kits Solutions



3380D C-M FT/CP solution -  
The cable mount kits are compatible with  
3360D/3360P cable mount FT/CP



3380D D-M CP solution -  
The direct mount kits are compatible with  
3360P/3380P D-M probe card

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